

# HOW EDIS USES THE DATA CAPTURED FOR REPORTING ELECTRICAL COMPLIANCE

After reading this document you should understand how EDIS uses data captured during electrical inspection and testing to report on electrical compliance. The document explains how the test coverage is calculated and a best practice for entering data into key fields to ensure the resulting compliance reports provide a view that allows data driven decision-making during the planning, reporting and execution of electrical compliance programmes.

### Contents

PURPOSE OF THIS DOCUMENT
ELECTRICAL COMPLIANCE REQUIREMENT2
THE EDIS CYCLE
IDENTIFYING THE DISTRIBUTION BOARDS AND CIRCUITS THAT REQUIRE INSPECTION AND TESTING 3
TESTING THE DISTRIBUTION BOARDS5
Entering test results and observations5
COMPLETING THE CERTIFICATE AND UPDATING THE COMPLIANCE DETAILS
The RESULTING DATA UPDATES7
Last/next test dates for the circuits7
Observations and recommendations8
AFTER TESTING AND CERTIFICATE COMPLETION9
Circuit results9
Distribution board Schedule updated10
THE UPDATED COMPLIANCE REPORTS11
CONCLUSION AND RECOMMENDATIONS





# PURPOSE OF THIS DOCUMENT

The primary purpose of the document is to illustrate how the data entered into the EDIS system enables better electrical compliance decision making and reporting, if the correct format is used.

The document demonstrates the end-to-end EDIS cycle, and how the recording of **test results influences the last test/ next test date**, and how the **last test/next test date affects the compliance reporting**.

For context purposes the introduction to the document provides a very brief explanation of the electrical compliance requirements and the EDIS cycle. The Document also provides a 'worked example' using a single distribution board.

# ELECTRICAL COMPLIANCE REQUIREMENT

An electrical compliance process requires a risk assessment, this is typically done in the form of an EICR (Electrical Inspection Condition Report), the results are captured into a format described in BS7671 Condition Report model forms. The report provides a commentary and assessment of the condition of the electrical installation.

## THE EDIS CYCLE

The EDIS cycle is a system and process to facilitate the reporting and status of electrical compliance.



The EDIS Cycle automates the data handling for this process by tracking the inspections, actions required following from Observations.

The cycle starts with a compliance requirement that identified what needs to be Inspected and Tested. These items are inspected and the condition of these is then recorded in a certificate. After testing and remediation, the compliance can then be reported.

Managing, tracking and reporting on the compliance requirement:

EDIS identified and managed the compliance requirement in two ways:



- 1. Tracking the last test date of a circuit (and the recommended next test date), the circuits last test date is used to determine the distribution board test dates.
- 2. Tracking the observations and associated actions, this is done via the Action Require list which is created when Observation and recommendations are included in an EICR

If the next test date is in the future and there are no incomplete actions, it implies that the system has been inspected and any recommendations have been remediated or re-assigned.

As time passes the system needs to be re-tested, and the compliance reports are updated.

# IDENTIFYING THE DISTRIBUTION BOARDS AND CIRCUITS THAT REQUIRE INSPECTION AND TESTING

To identify which distribution boards, need to be tested compliance report with different levels of detail can be generated.

The report below is for a single building and it reports that there are 21 circuits due for testing in the Belsize building.

							INTERI	I STATI	JS REPO	ORT AS	AT 30 DECEM	3ER 2023			
Estate:	Demo Estat	9													
											Energised Ccts	Ccts Not		Last 5	Latest
	Test										(Total non-	Due	Ccts Due	Inspection	Inspected
Building Name 🔻	Coverac -	C1 -	C2 -	C3 -	FI -	LIM 👻	NV -	FIO -	NCFI -	ERR -	spare) 👻	for testin 👻	for testing *	Dates -	Certificate
Belsize	0%	0	0	0	0	0	0	0	0	0	21	0	21	30/01/2019	11500

A more detailed compliance report displaying board/circuit details can be generated to identify specific boards and circuits that need testing. As an example, the distribution board schedule below shows the status of the Last test and Next test dates prior to testing. In this example **the last test date is 01/01/2019**, with a next test date set for 01/01/2024

						C	<b>R-0</b>	1/1	DB								
Circuit Number and Phase	Circuit	Type of wiring	Reference		Number of Points served	Cond	rcuit luctors :sa	:	onnection litted 1(s)	Over	currer	nt Prot	ection	Device	RCD	Maximum Z <sub>S</sub> permitted by BS 7671	Test Date -
Circuit and F	Designation	Typ wir	Refer		Points	Live (mm <sup>2</sup> )	cpc (mm	2)	Max. disconnection time permitted by BS 7671(s)	BS (EN)	Type No	Ratin (A)	g ca	Short- ircuit pacity (kA)	operating current, I <sub>Dn</sub> (mA)	Maximum Z <sub>S</sub> permitted by E	Next Test
1 - L1	Socket on Panel 1 & Lowerator	в	в		2	4	4		0.4	6089	вС	20		N/A	1	10	01/01/2019- 01/01/2024
1 - L2	Socket on Panel 3	в	в		1	4	4		0.4	6089	вС	20		N/A	1	10	01/01/2019- 01/01/2024
1 - L3	Socket on Panel 4	в	в		1	4	4	Τ	0.4	6089	вС	20		N/A	1	10	01/01/2019- 01/01/2024
2 - L1	Induction Hob 1 Socket	в	в		1	2.5	2.5	Τ	0.4	6089	вС	16		N/A	1.3	10	01/01/2019- 01/01/2024
2 - L2	Induction Hob 2 Socket	в	В		1	2.5	2.5		0.4	6089	вС	16		N/A	1.3	10	01/01/2019- 01/01/2024
2 - L3	Cold Well	в	в		1	2.5	2.5		0.4	6089	вС	16		N/A	1.3	10	01/01/2019- 01/01/2024
3 - L1	Soup & Lowerator	в	в		2	2.5/4	2.5/4	-	0.4	6089	вС	16		N/A	1.3	10	01/01/2019- 01/01/2024
3 - L2	Hot Cupboard Near	в	в		1	2.5	2.5		0.4	6089	вС	16		N/A	1.3	10	01/01/2019- 01/01/2024
3 - L3	Hot Cupboard Far	в	в		1	2.5	2.5		0.4	6089	вС	16		N/A	1.3	10	01/01/2019- 01/01/2024
4 - L1	Hot Plate 6	в	в		1	2.5	2.5	Τ	0.4	6089	вС	10		N/A	2.1	10	01/01/2019- 01/01/2024
4 - L2	Heat Lamps 6	в	в		1	2.5	2.5 2.5		0.4	6089	вС	10		N/A	2.1	10	01/01/2019- 01/01/2024
4 - L3	Hot Plate 5	в	В		1 2.5		2.5 2.5		0.4		вС	10		N/A	2.1	10	01/01/2019- 01/01/2024
5 - L1	Heat Lamps 5	в	в		3 2.5		.5 2.5		0.4		вС	10		N/A	2.1	10	01/01/2019- 01/01/2024
5 - L2	Hot Plate 4	в	в		1	2.5	2.5		0.4	6089	вС	10		N/A	2.1	10	01/01/2019- 01/01/2024
5 - L3	Heat Lamps 4		в	в	3		2.5	2.5	0.4	6	0898	с	10	N/A	2.1	10	01/01/2019-01/01/2024
6 - L1	Hot Plate 3		в	в	1		2.5	2.5	0.4	6	0898	с	10	N/A	2.1	10	01/01/2019-01/01/2024
6 - L2	Heat Lamps 3		в	в	3 3		2.5	2.5	0.4	6	0898	с	10	N/A	2.1	10	01/01/2019-01/01/2024
6 - L3	Hot Plate 2		в	в	1	1	2.5	2.5	0.4	6	0898	с	10	N/A	2.1	10	01/01/2019-01/01/2024
7 - L1	Heat Lamps 2		в	в	3		2.5	2.5	0.4	6	0898	с	10	N/A	2.1	10	01/01/2019-01/01/2024
7 - L2	Trayside Lights	6	в	в	1	$\uparrow$	1.5	1.5	0.4	6	0898	с	6	N/A	3.6	10	01/01/2019-01/01/2024
7 - L3	Plinth Lights		в	в	1	$\uparrow$	1.5	1.5	0.4	6	0898	с	6	N/A	3.6	10	01/01/2019-01/01/2024

**EDIS** 



## TESTING THE DISTRIBUTION BOARDS

During the testing of the distribution board, there are test readings and observations, in this example the observations are associated with a circuit. The schedule below shows the Circuit schedule of tests and provides an example of how the test results and observation can be recorded.

The example has been developed to illustrate how different situation can be handled when capturing the information. The information entered will determine how the circuit test dates and observations are handled by the EDIS system

Circuit Number	2	Phase	Sub-Main	TPN (Phases supply same point)	Circuit Designation	Polarity (Y/N)	Max Zs ELI Rec	Ro Trip Time (ms) x 1	CD Test Button Operation (Y/N)	AFDD Manual AFDD Test Button Operation (Y/N)	- Observation Group (Available for organisations)	Observation (Select or type text)	Classification Code: 1.2.3.4.5.FIO C1.C2.C3 N/V.LIM.NCFI
SOF	<u> </u>	_											
1	L	_1	N	N	Socket on Panel 1 & Lower						TEST LIMITATIONS	lim1- ACCESS RESTRICTED	FI
1	_	.2	Ν	N	Socket on Panel 3						TEST LIMITATIONS	lim2 - NOT ABLE TO OPEN BOARD	LIM
1	L	_3	Ν	N	Socket on Panel 4						TEST LIMITATIONS	Lim3 - could not locate	FI
2	l	_1	Ν	N	Induction Hob 1 Socket						-	-	-
2	L	.2	Ν	N	Induction Hob 2 Socket						-	-	-
2	L	_3	Ν	N	Cold Well						-	-	-
3	- L	_1	Ν	N	Soup & Lowerator						-	-	-
3	- L	.2	Ν	N	Hot Cupboard Near						-	-	-
3	L 1	.3	Ν	N	Hot Cupboard Far						-	-	-
4	- L	_1	Ν	N	Hot Plate 6	Y	0.11	N/A	N/A	N/A	TEST LIMITATIONS	Zs by calculation	FIO
4	<u>ا</u>	.2	Ν	N	Heat Lamps 6	Y	0.11	N/A	N/A	N/A	TEST LIMITATIONS	Zs by calculation	FIO
4	- L	.3	Ν	N	Hot Plate 5	Y	0.16	N/A	N/A	N/A	TEST LIMITATIONS	Zs by calculation	FIO
5	1	.1	Ν	N	Heat Lamps 5	Y	lim	N/A	N/A	N/A	TEST LIMITATIONS	Zs not possible due to	LIM
5	L	.2	Ν	N	Hot Plate 4	Y	lim	N/A	N/A	N/A	TEST LIMITATIONS	Zs not possible due to	LIM
5	L	.3	Ν	N	Heat Lamps 4	Y	lim	N/A	N/A	N/A	TEST LIMITATIONS	Zs not possible due to	LIM
6	L ا	_1	Ν	N	Hot Plate 3	Y	0.11	N/A	N/A	N/A		No RCD/RCBO Protection.	C3
6	L	.2	Ν	N	Heat Lamps 3	Y	0.11	N/A	N/A	N/A		Broken Socket outlet & No RCD/RCBO I	C2
6	L	.3	Ν	N	Hot Plate 2	Y	0.16	N/A	N/A	N/A		Broken Socket outlet & No RCD/RCBO I	C3
7	L	.1	Ν	N	Heat Lamps 2	Y	0.09	N/A	N/A	N/A		No RCD/RCBO Protection.	C3
7	L	2	Ν	N	Trayside Lights	Y	0.13	N/A	N/A	N/A		No RCD/RCBO Protection.	C3
7	L	3	Ν	Ν	Plinth Lights	Y	0.14	N/A	N/A	N/A		No RCD/RCBO Protection.	C4

#### Entering test results and observations

The table below explains how the different results will impact the circuit test date, i.e. if the test date is not updated, the circuit will be deemed NOT tested, the test dates are updated based on the Zs value, if there is no Zs value the test date is not updated, a reason should be provided for the missing values.

With reference to the table above

1	L1	The circuit was not accessible and converse of the circuit was not accessible and converse of the converse of the converse of the circuit was not accessible and conve	son for not testing has been e										
2	L1	No tests carried out No test results No comment on the reason <b>No Zs value implies the last/Next test date will NOT be updated and the circuit will be "untested"</b>											
3	L1	Same as 2L1: No tests carried out No test results No comment on the reason											



		No Zs value implies the last/Next test date will NOT be updated and the circuit will be "untested"
4	L1	Test carried out Some limitations, Note there is a value in the Zs field Limitation described Cat Code: FIO Zs value implies the last/Next test date WILL be updated and the circuit will be set as "tested"
5	L1	Test carried out Some limitations, Note the LIM value in the Zs field Limitation described Cat Code: LIM Zs value implies the Last/Next test date WILL be updated and the circuit will be set as "tested"
6	L1	Test carried out No Material Limitations Any comments and findings described in the Observations <b>Zs value implies the Last/Next test date WILL be updated and the circuit will be set</b> <b>as "tested"</b>
7	L1	Test carried out No Material Limitations Any comments and findings described in the Observations Note that the Cat Code for 7 L3- is an undefined value, i.e. C4, this will be flagged as an ERR in the report. <b>Zs value implies the Last/Next test date WILL be updated and the circuit will be set as "tested</b> "

# COMPLETING THE CERTIFICATE AND UPDATING THE COMPLIANCE DETAILS

The CASE 3 for completion is applied. Note these can be overwritten at the time of certificate completion. In this case. The data in the certificate schedules will replace the data in the final board schedule; if the Zs=0 or blank the last test next test date will not be updated because the assumption is that the the circuit has not been tested if there is no value. If there is a



# value, e.g. numeric or LIM, the system will assume that the circuit has been tested and update the last/next test dates.

$\odot$ CASE 1 - (Default) Test data retained; Changes to circuit details only if Zs	s>0 or Zs=lim; Last test/Next test date updated if Zs>0
If Zs in tested circuit >0 the data in the certificate schedules will replace the data in the final board schedule. Last test/Next test date for the circuit will only be updated if Zs>0; if the Zs=blank no changes will be applied in the final board schedule; if Zs=Lim the last test next test date will not be updated.	Use if you want the circuit details to be the same as the final circuit details except where Zs =0 or Zs=blank; e.g. if Zs is blank, and the circuit description is "Not Tested", the circuit description will not appear on the final schedule, that is, the final schedule will retain the original circuit description because Zs=blank.
○ CASE 2 - Test data retained; No change to circuit details; Only last test/N	lext test date updated if Zs>0 or Zs =lim
No changes in the tested circuit details will be applied to the final board schedule. Test data will be retained and if Zs>0 or Zs=lim the last test / next test date in the final schedule will be updated.	Use if you do not want to update any circuit details. (Note: the last test date will be updated if the Zs>0 or Zs=lim no other changes will be updated in the final schedule)
CASE 3 - Test data retained; Changes to circuits details regardless of Zs	value; Last test/Next test date updated if Zs>0 or Zs=lim
The data in the certificate schedules will replace the data in the final board schedule; if the Zs=0 or blank the last test next test date will not be updated because the circuit has not been tested.	Use if you want all circuit details in the certificate to appear in the final schedule.
$^{igodoldoldoldoldoldoldoldoldoldoldoldoldol$	value; Last test/Next test date updated regardless of Zs value
All data in the certificate schedules will replace the all data in the final board schedule.	Use if you want the test schedule to be exactly the same as the final schedule and you want the last test/next test dates on the circuit to be updated for all circuits.

# The RESULTING DATA UPDATES

#### Last/next test dates for the circuits

The resulting distribution board schedule and changes in the last/next test dates for the above example are shown below. Based on the CASE 3 scenario; any circuits that have no Zs value are not updated, and last/next test dates remain unchanged.

	CR-01/1 DB																
Number hase	Circuit	Type of wiring	teference	2	Number of Points served	Cond	cuit uctors sa	:	onnection itted 1(s)	Over	curre	ent Pr	otecti	on Device	RCD	Maximum Z <sub>S</sub> permitted by BS 7671	Test Date -
Circuit Number and Phase	Designation	Type of wiring	Reference		Number of Points serve	Live (mm <sup>2</sup> )	cpc (mm		Max. disconnection time permitted by BS 7671(s)	BS (EN)	Ty; No		ting A)	Short- circuit capacity (kA)	operating current, I <sub>Dn</sub> (mA)	Maximum Z <sub>S</sub> permitted by I	Next Test
1 - L1	Socket on Panel 1 & Lowerator	в	В		2	4	4		0.4	6089	вС	: :	20	N/A	1	10	01/01/2019- 01/01/2024
1 - L2	Socket on Panel 3	в	в		1	4	4		0.4	6089	вС	: :	20	N/A	1	10	01/01/2019- 01/01/2024
1 - L3	Socket on Panel 4	в	в		1	4	4		0.4	6089	вС	:	20	N/A	1	10	01/01/2019- 01/01/2024
2 - L1	Induction Hob 1 Socket	в	в		1	2.5	2.5		0.4	6089	вС	; .	16	N/A	1.3	10	01/01/2019- 01/01/2024
2 - L2	Induction Hob 2 Socket	В	В		1	2.5	2.5		0.4	6089	вС	;	16	N/A	1.3	10	01/01/2019- 01/01/2024
2 - L3	Cold Well	в	в		1	2.5	2.5		0.4	6089	вС	; .	16	N/A	1.3	10	01/01/2019- 01/01/2024
3 - L1	Soup & Lowerator	в	в		2	2.5/4	2.5/	4	0.4	6089	вС	;	16	N/A	1.3	10	01/01/2019- 01/01/2024
3 - L2	Hot Cupboard Near	в	в		1	2.5	2.5	T	0.4	6089	вС	;	16	N/A	1.3	10	01/01/2019-01/01/2024
3 - L3	Hot Cupboard Far	в	в		1	2.5	2.5	T	0.4	6089	вС	;	16	N/A	1.3	10	01/01/2019- 01/01/2024
4 - L1	Hot Plate 6	в	в		1	2.5	2.5		0.4	6089	вС	; .	10	N/A	2.1	10	29/01/2024- 29/01/2029
4 - L2	Heat Lamps 6	в	в		1	2.5	2.5	T	0.4	6089	вС	; .	10	N/A	2.1	10	29/01/2024- 29/01/2029
4 - L3	Hot Plate 5	в	в		1	2.5	2.5		0.4	6089	вС	; .	10	N/A	2.1	10	29/01/2024- 29/01/2029
5 - L1	Heat Lamps 5	в	в		3	2.5	2.5		0.4	6089	вС	; .	10	N/A	2.1	10	29/01/2024- 29/01/2029
5 - L2	Hot Plate 4	в	в		1	2.5	2.5	T	0.4	6089	вС	; .	10	N/A	2.1	10	29/01/2024- 29/01/2029
5 - L3	Heat Lamps 4		в	в	3	2	.5	2.5	0.4	60	898	с	10	N/A	2.1	10	29/01/2024-
6 - L1	Hot Plate 3	+	в	в	1	2	.5	2.5	0.4	60	898	С	10	N/A	2.1	10	29/01/2029 29/01/2024-
6 - L2	Heat Lamps 3		в	в	3	2	.5	2.5	0.4	60	898	С	10	N/A	2.1	10	29/01/2029 29/01/2024-
6 - L3	Hot Plate 2	-+	в	в	1	2	.5	2.5	0.4	60	898	С	10	N/A	2.1	10	29/01/2029 29/01/2024-
7 - L1	Heat Lamps 2	-+	в	в	3	2		2.5	0.4	+	898	С	10	N/A	2.1	10	29/01/2029 29/01/2024-
7 - L2	Trayside Lights		в	в	1	1.	-	1.5	0.4	+	898	C	6	N/A	3.6	10	29/01/2029 29/01/2024-
7 - L3	Plinth Lights	+	в	в	1	1.	+	1.5	0.4	+	898	С	6	N/A	3.6	10	29/01/2029 29/01/2024-
	g											-					29/01/2029

2

#### Observations and recommendations

The Observations and recommendation for the certificate is shown in the Excel export of Observations and Recommendations below:



F	G	Н	l I	J	K
ltem					Severity
Number -	Board/Circuit -	🕆 Estate Remedial Group 🚽 👻	Observation Group	Item Text 👻	Code 👻
				lim1- ACCESS	
1	CR-01/1 DB, 1 L1		TEST LIMITATIONS	RESTRICTED	FI
				lim2 - NOT ABLE TO OPEN	
2	CR-01/1 DB, 1 L2		TEST LIMITATIONS	BOARD	FI
3	CR-01/1 DB, 1 L3		TEST LIMITATIONS	Lim3 - could not locate	FI
10	CR-01/1 DB, 4 L1		TEST LIMITATIONS	Zs by calculation	FIO
11	CR-01/1 DB, 4 L2		TEST LIMITATIONS	Zs by calculation	FIO
12	CR-01/1 DB, 4 L3		TEST LIMITATIONS	Zs by calculation	FIO
13	CR-01/1 DB, 5 L1		TEST LIMITATIONS	Zs not possible due toal	LIM
14	CR-01/1 DB, 5 L2		TEST LIMITATIONS	Zs not possible due toal	LIM
15	CR-01/1 DB, 5 L3		TEST LIMITATIONS	Zs not possible due toal	LIM
4	CR-01/1 DB, 6 L1			No RCD/RCBO Protection.	C3
				Broken Socket outlet & No	
5	CR-01/1 DB, 6 L2			RCD/RCBO Protection.	C2
				Broken Socket outlet & No	
6	CR-01/1 DB, 6 L3			RCD/RCBO Protection.	C3
7	CR-01/1 DB, 7 L1			No RCD/RCBO Protection.	C3
8	CR-01/1 DB, 7 L2			No RCD/RCBO Protection.	C3
9	CR-01/1 DB, 7 L3			No RCD/RCBO Protection.	C3

# AFTER TESTING AND CERTIFICATE COMPLETION

The overall results are summarised as follows:

#### Circuit results

Iumber of boards: 1 Total number of circuits:				21	Number of SPARE circuits:	Ē	0		
Number of boards where Zdb is a numeric value:		Numbe excludii		cuits with a Zs RES	9	Number of circuit where Zs=LIM, e SPARE:		3	
			nk or C	cuits where , excluding	9	Number of circuit other value, exclu SPARES:		0	
Board Designati	on	Phase	Ways	Board Type		Section	Floor		Location
CR-01/1 DB	3	7							

#### CIRCUITS DUE BEFORE TEST:

21 Circuits due for testing 01/01/2024

#### **CIRCUITS DUE AFTER TEST:**

9 x circuits due for testing 01/01/2024 (12 circuits tested, 29/01/2024 and due in 29/02/2030)

#### **TEST LIMITATIONS:**

3 x circuits -deemed not tested (Zs= blank), with a TEST LIMITATION explanation (no access)

- 3 x circuits deemed not tested (Zs= blank), with no explanation (no data, no observation)
- 6 x-deemed tested (Zs has a value), with a limitation or a "For Info Only" (FIO) notice
- 6 x-deemed tested with no limitation (Zs has a value), but with an observation



The results are reported in EDIS, there are views for each stakeholder:

#### PDF Certificate

The example below shows the Schedule of Test results, values entered and notes for the circuits.

	TO BE C	OMPLETE					RD IS NOT CO	NNECTED DIR	ECTLY					TEST INSTRUMENTS (SERIAL NUMBERS) USED
	ution bo ation:	ard CF	-01/1 DB								arth faul		mft-00	RCD n/a
s at l	OB 0	Ω				с	ONFIRMED				nsulation resistance		n/a	Other N/A
pf at l	DB 0	kΑ	Corre Suppl		Phas Seq	se uence:		SPD ¶ Operation Status :	nal N//		Continuity		n/a	Other N/A
			r installe when test		nent									Next test date: 29/01/2029
_		Circuit	mpedar	ices (Ω)		istance	Insulation Record lower		Polarity	Zs	RC	D	AFDD	Remarks
Circuit number and phase		inal Con sured end (Ω)	to end ]	Conti [ at lea column compli (C	st one to be eted ]	Insulation Resis Test Voltage	Live/Live	Live/Earth		Maximum Meas ured	Disconnection Time at IΔn ‡	Test Button Operation	Manual AFDD test button operation	Include details of circuits and/or installed equipment vulnerable to damage when testing
-	r1 (Line)	rn (Neutral	r2 ) (cpc)	R1+R2	R2	⊆ ⊢ (V)	(MΩ)	(MΩ)	# (√)	ΣΣ (Ω)	(ms)	(√)	(√)	
1 L1	-	-	-		-	-	-	-	-		-	-	-	TEST LIMITATIONS : lim1- ACCESS RESTRICTED - FI
1 L2	-	-		1.1	-	-	-	-	-			-		TEST LIMITATIONS : Im2 - NOT ABLE TO OPEN BOARD - LIM
L3	-	-			-	-		-	-			-		TEST LIMITATIONS : Lim3 - could not locate - FI
2 L1	-	-			-	-	-	-	-			-		•
2 L2	-	-			-	-	-	-	-			-		•
2 L3	-	-				-		-				-		
3 L1	-	-		-		-		-	-		-	-	-	
3 L2	-	-	-	-	-	-		-	-	-	-	-	-	-
3 L3	-	-	-	-		-	-	-	-		-	-	-	-
ш	N/A	N/A	N/A	0.1	-	500	LIM	LIM	1	0.11	N/A	N/A	N/A	TEST LIMITATIONS : Zs by calculation - FIO
L2	N/A	N/A	N/A	0.08	-	500	LIM	LIM	1	0.11	N/A	N/A	N/A	TEST LIMITATIONS : Zs by calculation - FIO
4 L3	N/A	N/A	N/A	0.070	-	500	LIM	LIM	1	0.16	N/A	N/A	N/A	TEST LIMITATIONS : Zs by calculation - FIO

J. OBSERV	ATIONS AND RECOMMENDATIONS FOR ACTIONS TO BE TAKEN	_
Item No	Referring to the attached schedules of inspection and test results, and subject to the limitations at D: (The items in this section provide observations and recommendations for remedial actions.)	Clas sification Code
DICTRIBUT	ION BOARD AND CIRCUIT OBSERVATIONS	

ווצוט	(BUTION BOARD AND CIRCUIT OBSERVATIONS		
1	TEST LIMITATIONS: lim1- ACCESS RESTRICTED	CR-01/1 DB: 1 L1	FI
2	TEST LIMITATIONS: lim2 - NOT ABLE TO OPEN BOARD	CR-01/1 DB: 1 L2	FI
3	TEST LIMITATIONS: Lim3 - could not locate	CR-01/1 DB: 1 L3	FI
4	No RCD/RCBO Protection.	CR-01/1 DB: 6 L1	C3
5	Broken Socket outlet & No RCD/RCBO Protection.	CR-01/1 DB: 6 L2	C2
6	Broken Socket outlet & No RCD/RCBO Protection.	CR-01/1 DB: 6 L3	C3
7	No RCD/RCBO Protection.	CR-01/1 DB: 7 L1	С3
8	No RCD/RCBO Protection.	CR-01/1 DB: 7 L2	C3
9	No RCD/RCBO Protection.	CR-01/1 DB: 7 L3	C3
10	TEST LIMITATIONS: Zs by calculation	CR-01/1 DB: 4 L1	FIO
11	TEST LIMITATIONS: Zs by calculation	CR-01/1 DB: 4 L2	FIO
12	TEST LIMITATIONS: Zs by calculation	CR-01/1 DB: 4 L3	FIO
13	TEST LIMITATIONS: Zs not possible due to	CR-01/1 DB: 5 L1	LIM
14	TEST LIMITATIONS: Zs not possible due to	CR-01/1 DB: 5 L2	LIM
15	TEST LIMITATIONS: Zs not possible due to	CR-01/1 DB: 5 L3	LIM

#### Distribution board Schedule updated

The example shows the last/next test dates



CR-01/1 DB														
Circuit Number and Phase	Circuit	Type of wiring	Reference Method	Number of Points served	Circuit Conductors: csa		Max. disconnection time permitted by BS 7671(s)	Overcurrent Protection Device				RCD	Maximum Z <sub>S</sub> permitted by BS 7671	Test Date -
Circuit Numb and Phase	Designation				Live (mm <sup>2</sup> )	cpc (mm <sup>2</sup> )	Max. disconne time permitted by BS 7671(s)	BS (EN)	Type No	Rating (A)	Short- circuit capacity (kA)	operating current, I <sub>Dn</sub> (mA)	Maximum Z <sub>S</sub> permitted by	Next Test
1 - L1	Socket on Panel 1 & Lowerator	В	в	2	4	4	0.4	60898	С	20	N/A	1	10	01/01/2019- 01/01/2024
1 - L2	Socket on Panel 3	В	В	1	4	4	0.4	60898	С	20	N/A	1	10	01/01/2019- 01/01/2024
1 - L3	Socket on Panel 4	в	в	1	4	4	0.4	60898	с	20	N/A	1	10	01/01/2019- 01/01/2024
2 - L1	Induction Hob 1 Socket	В	В	1	2.5	2.5	0.4	60898	с	16	N/A	1.3	10	01/01/2019- 01/01/2024
2 - L2	Induction Hob 2 Socket	В	В	1	2.5	2.5	0.4	60898	с	16	N/A	1.3	10	01/01/2019- 01/01/2024
2 - L3	Cold Well	В	В	1	2.5	2.5	0.4	60898	с	16	N/A	1.3	10	01/01/2019- 01/01/2024
3 - L1	Soup & Lowerator	в	В	2	2.5/4	2.5/4	0.4	60898	с	16	N/A	1.3	10	01/01/2019- 01/01/2024
3 - L2	Hot Cupboard Near	В	В	1	2.5	2.5	0.4	60898	с	16	N/A	1.3	10	01/01/2019- 01/01/2024
3 - L3	Hot Cupboard Far	В	В	1	2.5	2.5	0.4	60898	с	16	N/A	1.3	10	01/01/2019- 01/01/2024
4 - L1	Hot Plate 6	в	В	1	2.5	2.5	0.4	60898	с	10	N/A	2.1	10	29/01/2024- 29/01/2029
4 - L2	Heat Lamps 6	В	В	1	2.5	2.5	0.4	60898	с	10	N/A	2.1	10	29/01/2024- 29/01/2029
4 - L3	Hot Plate 5	В	В	1	2.5	2.5	0.4	60898	с	10	N/A	2.1	10	29/01/2024- 29/01/2029
5 - L1	Heat Lamps 5	В	В	3	2.5	2.5	0.4	60898	с	10	N/A	2.1	10	29/01/2024- 29/01/2029
5 - L2	Hot Plate 4	В	В	1	2.5	2.5	0.4	60898	с	10	N/A	2.1	10	29/01/2024- 29/01/2029

# THE UPDATED COMPLIANCE REPORTS

<b>BEFORE:</b>															
							INTERI	M STAT	US REPO	ORT AS	AT 30 DECEM	BER 2023			
Estate:	Demo Estat	е													
											Energised Ccts	Ccts Not		Last 5	Latest
Building Name	Test Coverac •	C1 👻	C2 -	C3 -	FI 💌	LIM -	NV -	FIO -	NCFI	ERR -	(Total non-	Due for testin	Ccts Due for testing	Inspection Dates	Inspected Certificate
Belsize	0%	0	0	0	0	0	0	0	0	0	21	0	21	30/01/2019	11500
AFTER:															
Estate:	INTERIM STATUS REPORT AS AT 30 JANUARY 2024 Demo Estate														
Building Name	Test Coverage	C1	C2	C3	FI	LIM	NV	FIO	NCFF	ERR	Energised Ccts (Total non- spare)	Ccts Not Due for testing	Ccts Due for testing	Last 5 Inspection Dates	Latest Inspected Certificates
Belsize	57%	0	1	5	3	3	0	3	0	0	21	12	9	30/01/2019	11500

This summarises:

- The test coverage i.e. 12 circuits tested out of 21.
- The number of Observations, specifically 3 x FI's, that have not had access.

-----



# CONCLUSION AND RECOMMENDATIONS

This document is not a replacement for good electrical engineering judgment or electrical wiring standards; It's only aims to illustrate how the EDIS system can assist in providing better process and information for improving electrical compliance in a building.

Following the data recording described in this document will lead to better electrical compliance reporting.